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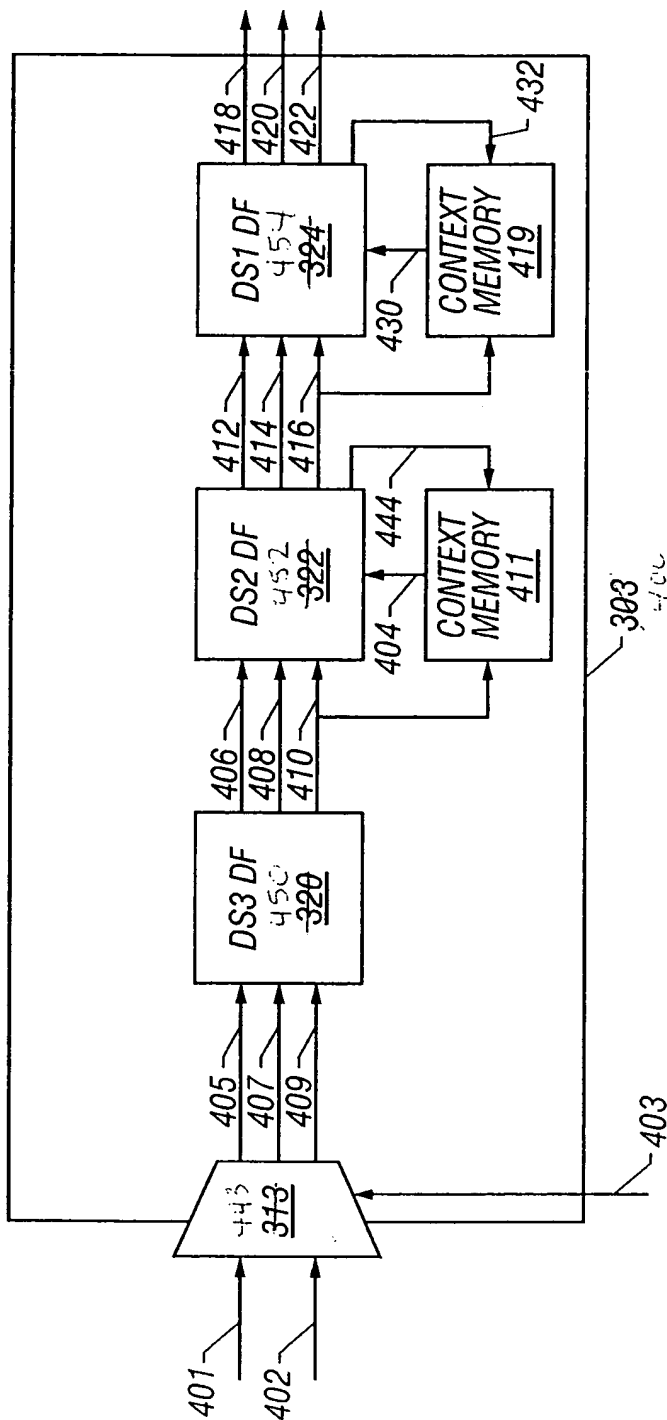


FIG. 4A

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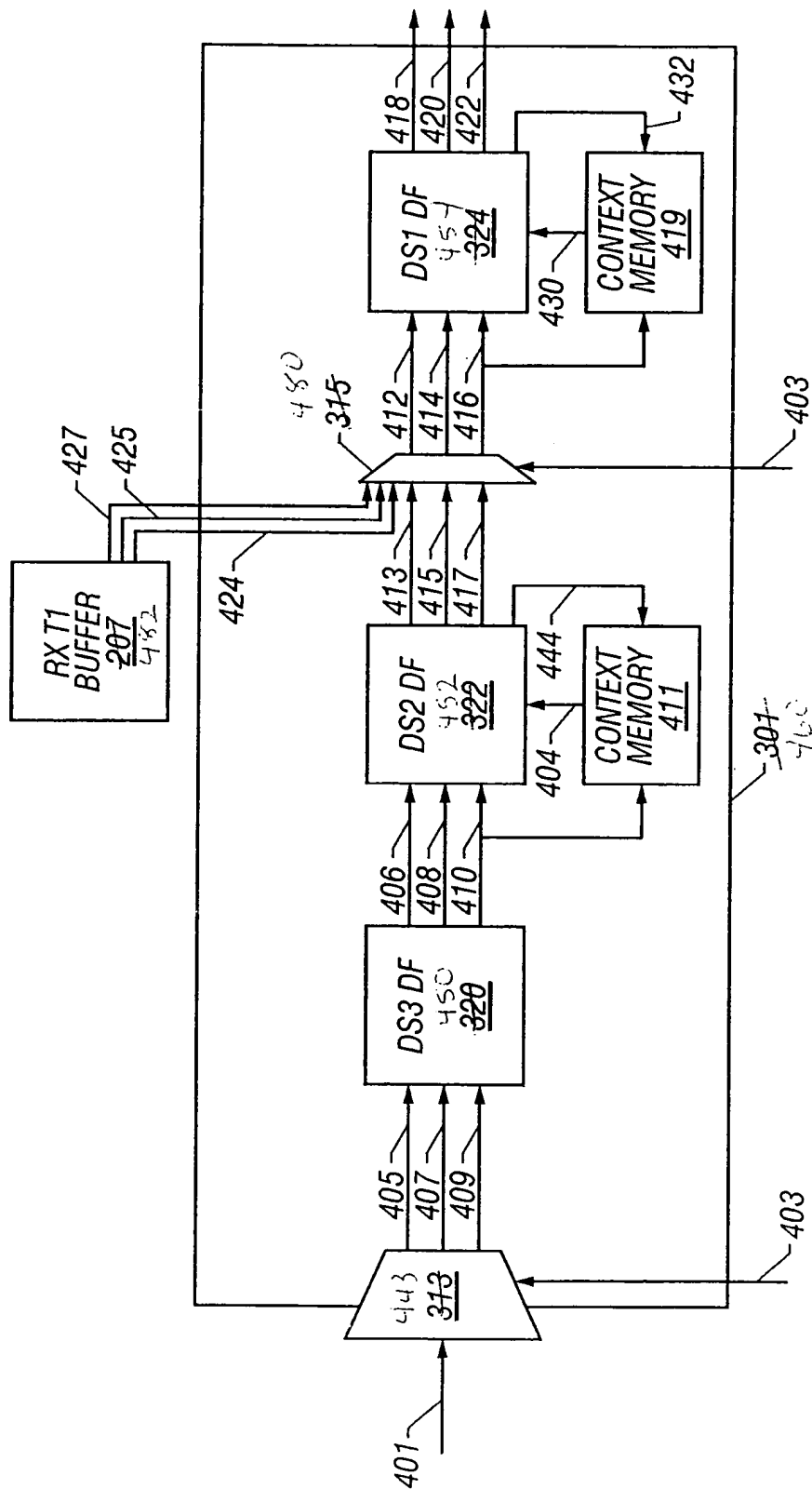


FIG. 4B

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09/835,979
METHOD AND APPARATUS FOR
SIMULTANEOUSLY SYNC HUNTING SIGNALS
Michael McClary, et al.

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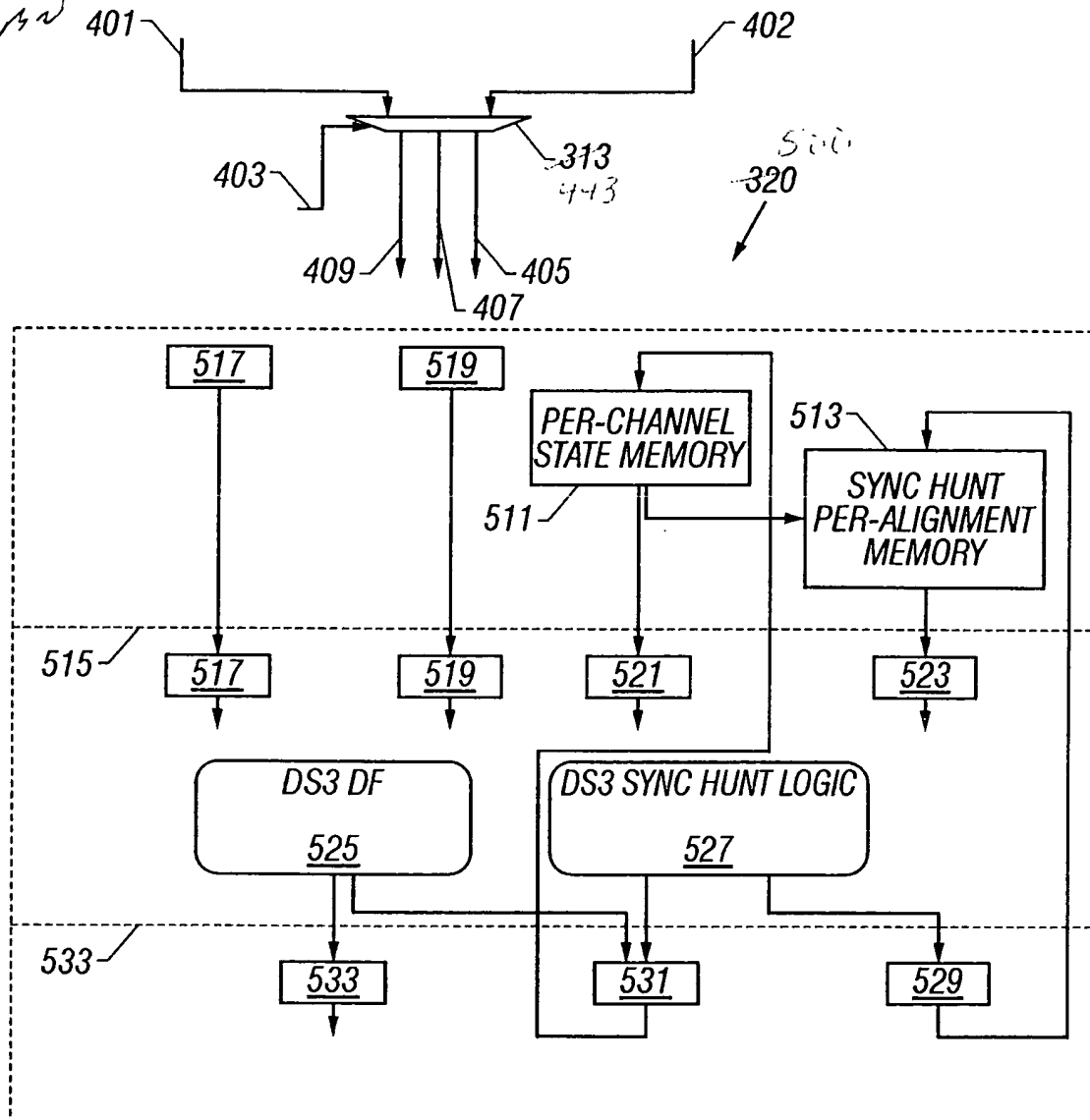


FIG. 5



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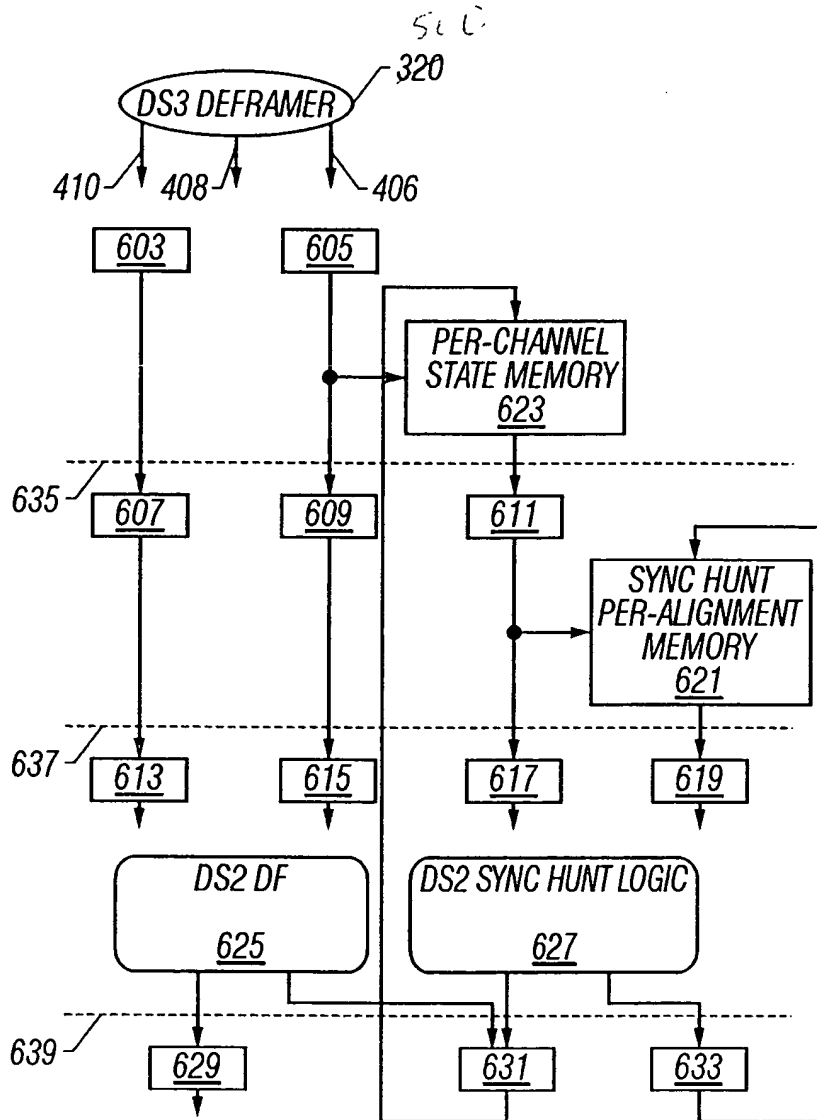


FIG. 6

FIG. 1 is a block diagram of a DS2 to DS1 multiplexer system. The system includes a DS2 DEFRAMER (322) which outputs three channels (416, 414, 412). Channel 416 passes through blocks 701, 705, and 711 to DS1 DF (725). Channel 414 passes through blocks 703, 707, and 713 to DS1 DF (725). Channel 412 passes through PER-CHANNEL STATE MEMORY (719), block 709, and 715 to DS1 SYNC HUNT LOGIC (727). Both DS1 DF (725) and DS1 SYNC HUNT LOGIC (727) output to block 729. Block 729 outputs to a multiplexer (735) which also receives input from block 733. The multiplexer (735) outputs to a DS1MC block (741). There are also two intermediate multiplexers (737 and 740) that receive inputs from the state memory and other blocks, and output to the DS1MC block.

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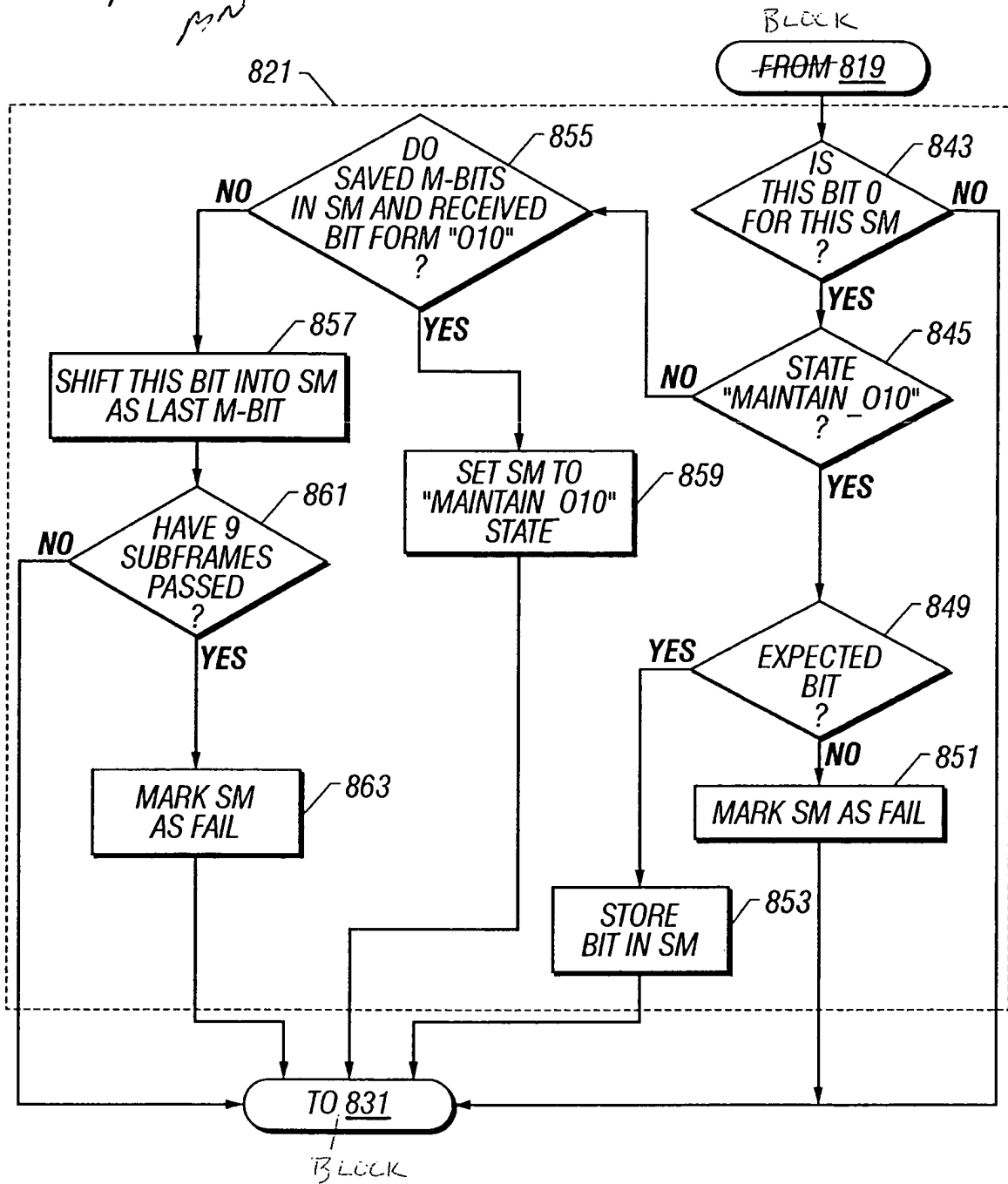


FIG. 8B

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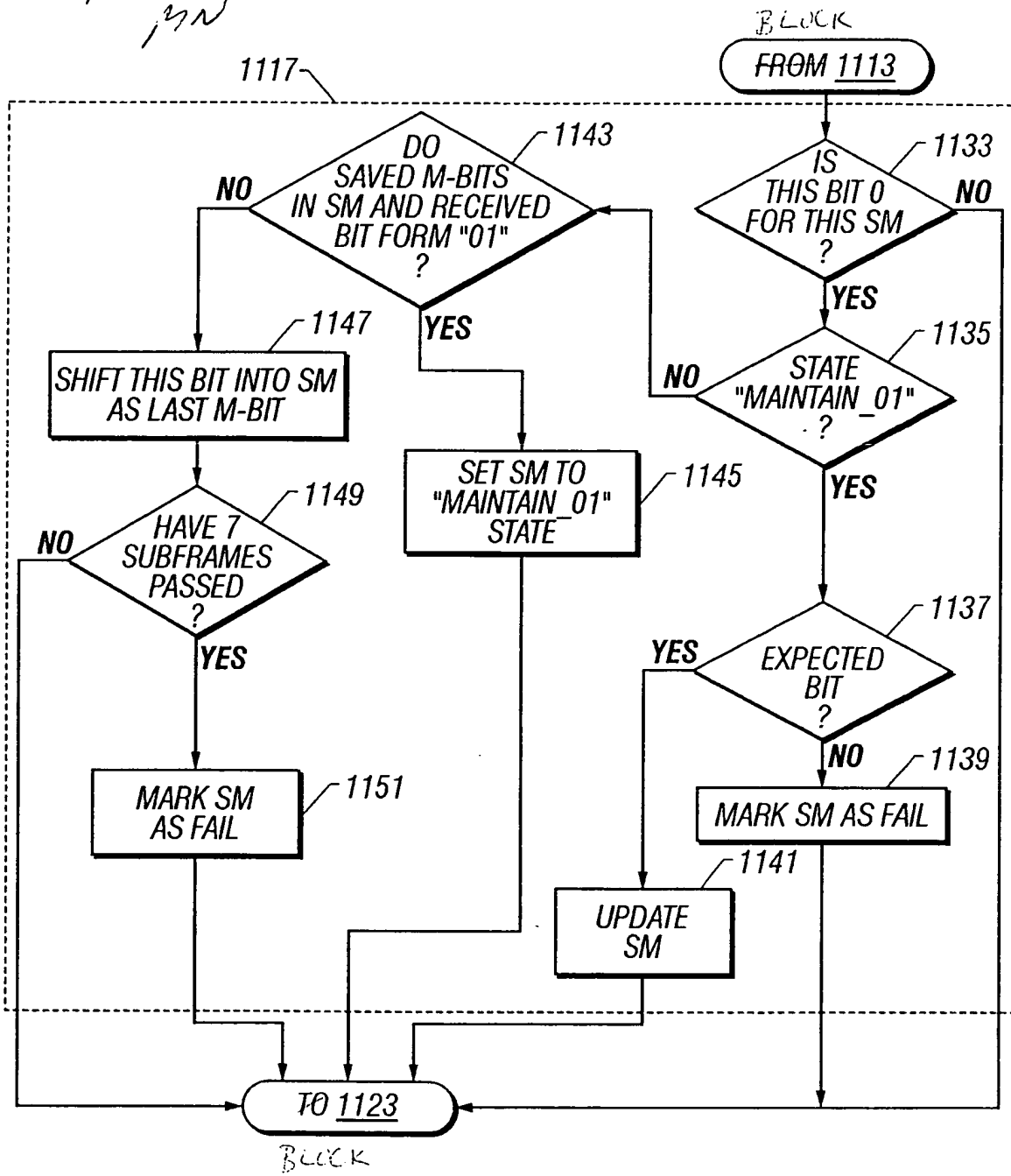


FIG. 11B



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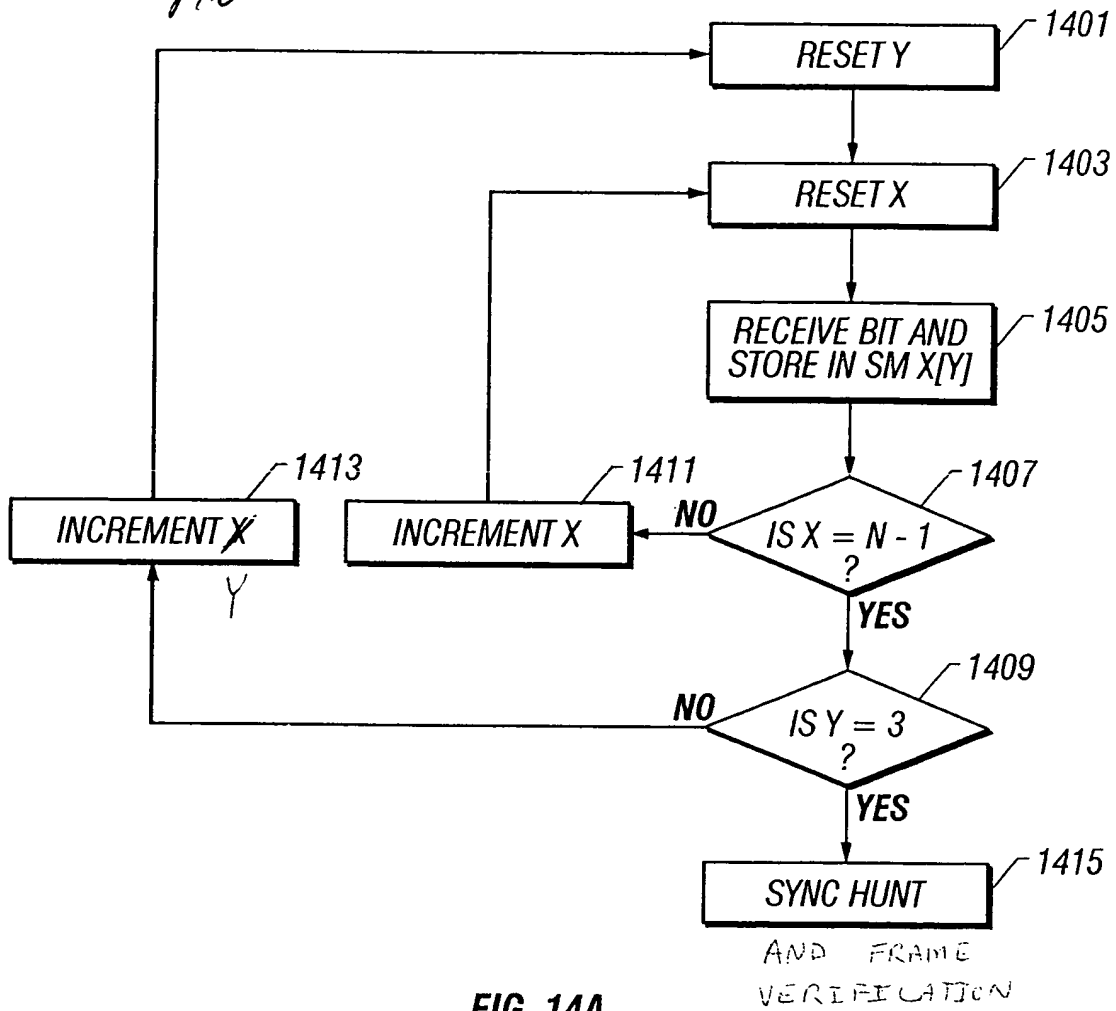


FIG. 14A



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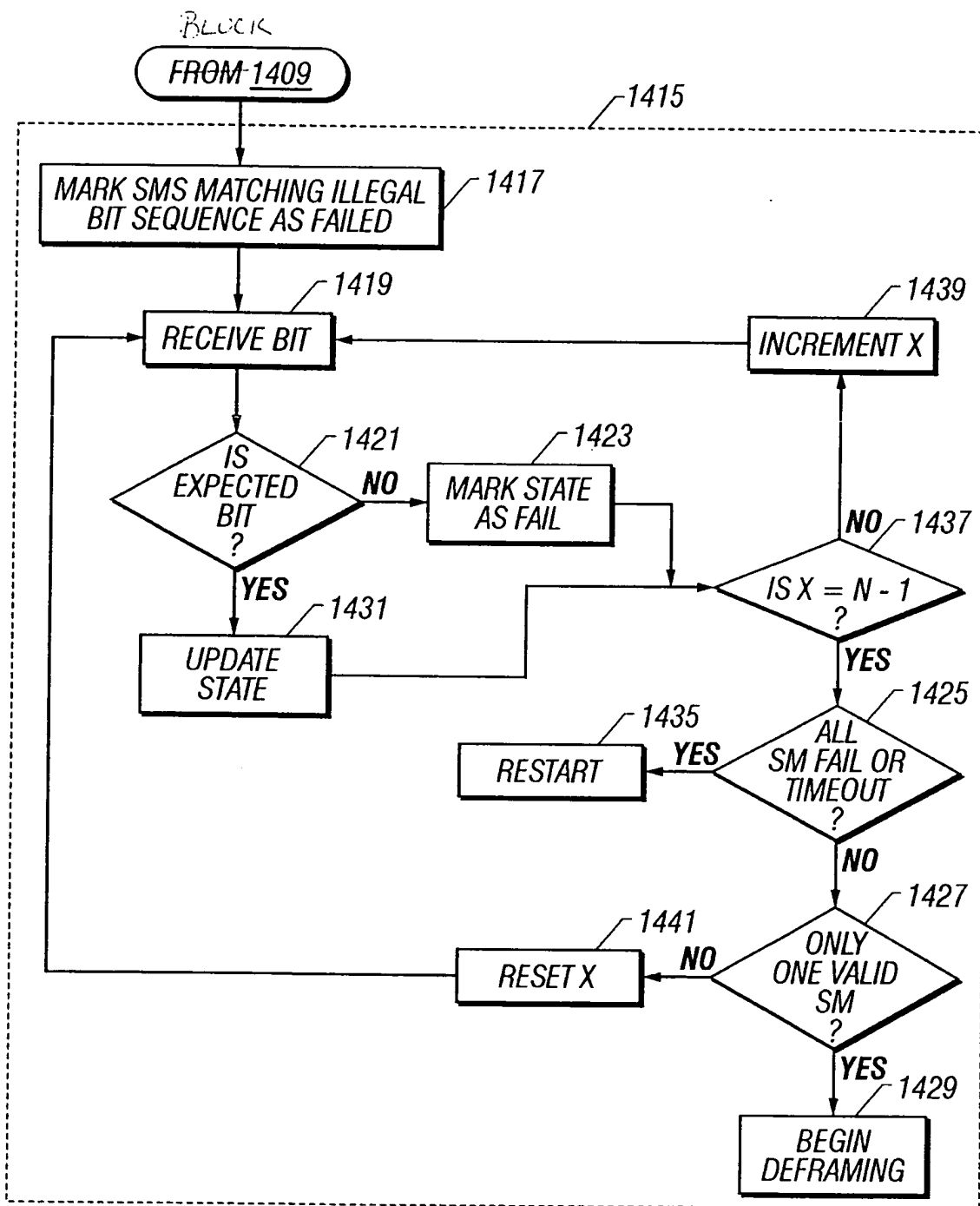


FIG. 14B



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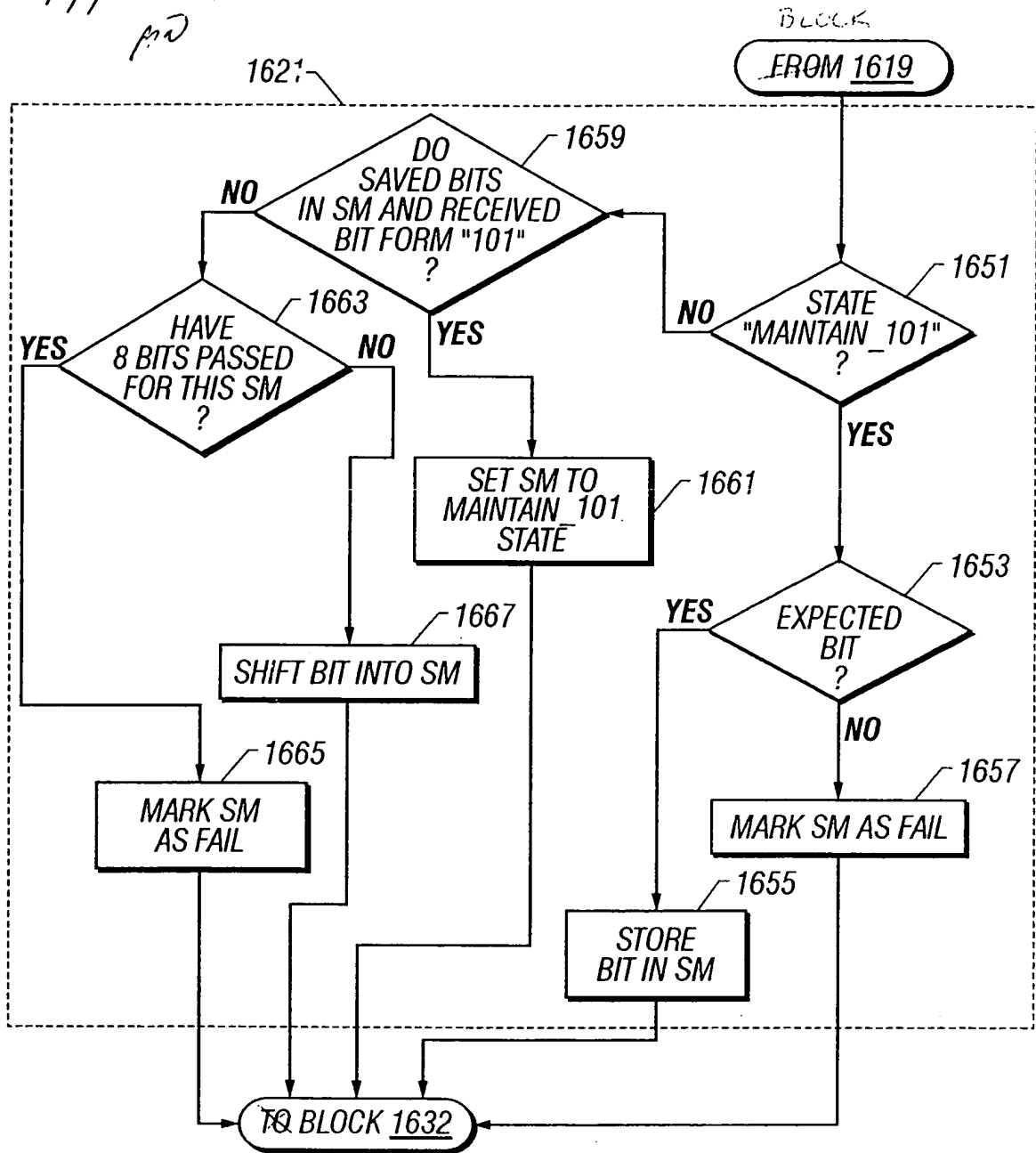


FIG. 16B